Low Memory Attacks on Small Key CSIDH

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1 REGA overview



3 Adapting Techniques to the REGA-DLOG_m Setting



Potential Impact on Bit Security Level



Group Action

Let (\mathcal{G}, \circ) be a group with identity element $\mathit{id} \in \mathcal{G}$, and \mathcal{X} a set. A map

 $\star:\mathcal{G}\times\mathcal{X}\to\mathcal{X}$

is a group action if it satisfies the following properties:

- 1. Identity: $id \star x = x$ for all $x \in \mathcal{X}$.
- **2**. Compatibility: $(g \circ h) \star x = g \star (h \star x)$ for all $g, h \in \mathcal{G}$ and $x \in \mathcal{X}$.

Restricted Effective Group Action

Let $(\mathcal{G}, \mathcal{X}, \star)$ be a group action and let $\mathbf{g} = (g_1, ..., g_n)$ be a set of elements in \mathcal{G} and denote $\mathcal{H} = \langle g_1, ..., g_n \rangle$ for the subgroup generated by these elements. Assume that the following properties are satisfied:

- 1. *G* is finite, commutative, and n = poly(log(#H)).
- 2. $\mathcal X$ is finite, and there exist efficient algorithms for membership testing and computing a unique representation.
- 3. There exists a distinguished element $\tilde{x} \in \mathcal{X}$ with known representation.
- 4. There exists an efficient algorithm that given $g_i \in \mathbf{g}$ and $x \in \mathcal{X}$, outputs $g_i \star x$ and $g_i^{-1} \star x$.

Then we call $(\mathcal{G}, \mathcal{H}, \mathcal{X}, \star, \tilde{x})$ a restricted effective group action (REGA).



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Vector representation. Let $(\mathcal{G}, \mathcal{H}, \mathcal{X}, \star, \tilde{x})$ be a REGA with $\mathbf{g} = (g_1, \ldots, g_n)$. Elements in \mathcal{H} can be represented as vectors $\mathbf{v} \in \mathbb{Z}^n$ under the mapping $\phi : \mathbb{Z}^n \to \mathcal{H}$, where

$$\phi: \mathbf{v} = (\mathbf{v}_1, \ldots, \mathbf{v}_n) \mapsto \prod_{i=1}^n g_i^{v_i}.$$

Via the map ϕ , we define the action of \mathbb{Z}^n on \mathcal{X} . Slightly abusing notation, we denote $\mathbf{v} \star \mathbf{x} = \phi(\mathbf{v}) \star \mathbf{x}$.



1 REGA overview

2 REGA-based Diffie-Hellman protocol

Adapting Techniques to the REGA-DLOG_m Setting



Potential Impact on Bit Security Level





Figure: A REGA-based Diffie-Hellman protocol.

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- 1. GA-DLOG: Given $(x, y) \in \mathcal{X}^2$, determine $g \in \mathcal{G}$ such that $y = g \star x$.
- 2. GA-CDH: Given $(x, y, z) \in \mathcal{X}^3$, find $w \in \mathcal{X}$ such that there exists $g \in \mathcal{G}$ with $y = g \star x$ and $w = g \star z$.

Group actions satisfying these hardness assumptions are known as cryptographic group actions [1].





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- 2. GA-CDH: Given $(x, y, z) \in \mathcal{X}^3$, find $w \in \mathcal{X}$ such that there exists $g \in \mathcal{G}$ with $y = g \star x$ and $w = g \star z$.
- 3. REGA-DLOG_{SK}: Given $(x, y) \in \mathcal{X}^2$, determine $\mathbf{v} \in SK$ such that $y = \mathbf{v} \star x$ if such a vector \mathbf{v} exists.

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Low Memory Attacks on Small Key CSIDH

Lemma

Let $(\mathcal{G}, \mathcal{H}, \mathcal{X}, \star, \tilde{x})$ be a REGA with $\boldsymbol{g} = (g_1, \dots, g_n)$. Let $m \in \mathbb{N}$ and consider

 $\mathrm{SK}_1 = \{-m, \dots, m\}^n, \quad \mathrm{SK}_2 = \{0, \dots, 2m\}^n, \quad \text{and} \quad \mathrm{SK}_3 = \{-2m, -2(m-1), \dots, 2m\}^n.$

Then REGA-DLOG_{SK1} and REGA-DLOG_{SK2} are equivalent.

Further let $\tilde{\mathcal{H}} = \{g \circ g \mid g \in \mathcal{H}\} \subset \mathcal{H}$, and $\tilde{g} = (\tilde{g_1} = g_1 \circ g_1, \dots, \tilde{g_n} = g_n \circ g_n)$.

- 2. An instance $(\mathcal{G}, \mathcal{H}, \mathcal{X}, \star, \tilde{x}, \boldsymbol{g}, x, y)$ of REGA-DLOG_{SK3} can be transformed to an instance $(\mathcal{G}, \tilde{\mathcal{H}}, \mathcal{X}, \star, \tilde{x}, \boldsymbol{\tilde{g}}, x, y)$ of REGA-DLOG_{SK1}.
- 3. In particular if #H is odd, then REGA-DLOG_{SK3} reduces to REGA-DLOG_{SK1}.

Isogeny-based REGAs. The analysis in the original CSIDH paper [2] illustrates a practical example of a REGA, where

 \mathcal{G} is the ideal class group $cl(\mathcal{O})$ with $\mathcal{O} = \mathbb{Z}[\pi]$, \mathcal{H} is the subgroup generated by $\mathbf{g} = ([\mathfrak{l}_1], \dots, [\mathfrak{l}_n])$ with $\mathfrak{l}_i = (\ell_i, \pi - 1) \triangleleft \mathcal{O}$, \mathcal{K} is $\mathcal{E}\ell_p(\mathcal{O}) = \{E_A : y^2 = x^3 + Ax^2 + x \mid A \in \mathbb{F}_p \text{ and } E_A \text{ is supersingular}\},$ \star is the CSIDH group action, and \tilde{v} is the supersingular surve $E_{i} : y^2 = x^3 + x$ over \mathbb{F} .

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 \tilde{x} is the supersingular curve $E_0: y^2 = x^3 + x$ over \mathbb{F}_p .



1 REGA overview



Adapting Techniques to the REGA-DLOG_m **Setting**



Potential Impact on Bit Security Level

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Given $x, y \in \mathcal{X}$, we want to find $\mathbf{v} \in SK_1$ with $y = \mathbf{v} \star x$. Let us focus on the case m = 1 for simplicity. Let $N = \#\mathcal{H}, N_m = 3^n \ll N$, and $W = 3^{\omega n}$ for some $\omega \in [0, 0.5]$. Let

$$SK_1 = \{-1, 0, 1\}^n$$
, $SK_2 = \{0, 1, 2\}^n$, and $SK_3 = \{-2, 0, 2\}^n$.

- Pollard-style random walks based on [5, 4]. Time complexity: $\mathcal{O}(\sqrt{N})$;
- Meet-in-the-Middle (MitM). Memory and Time complexities: O(3^{0.5n}).
- Parallel Collision Search (PCS): Memory complexity $\widetilde{\mathcal{O}}(W)$, and Time complexity $\widetilde{\mathcal{O}}\left(3^{(0.75-0.5\omega)n}\right)$
- Representation-based Approach (This work): $\alpha = 1/3$ implies Memory complexity $\widetilde{\mathcal{O}}(W)$, and Time complexity $\widetilde{\mathcal{O}}\left(3^{(0.675-0.5\omega)n}\right)$ when $\omega \leq 0.22$.
- Partial Representation (This work):

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$$S_{m,0} := \{-1,0,1\}^{\frac{n}{2}} \times \{0\}^{\frac{n}{2}}, \text{ and } S_{m,1} := \{0\}^{\frac{n}{2}} \times \{1,0,1\}^{\frac{n}{2}}.$$

- Pollard-style random walks based on [5, 4]. Time complexity: $\mathcal{O}(\sqrt{N})$;
- Meet-in-the-Middle (MitM). Memory and Time complexities: O(3^{0.5n}). It reduces to finding two vectors v₀ ∈ S_{m,0} and v₁ ∈ S_{m,1} with v₀ * x = (−v₁) * y. The solution is v = v₀ + v₁.
- Parallel Collision Search (PCS): Memory complexity $\widetilde{\mathcal{O}}(W)$, and Time complexity $\widetilde{\mathcal{O}}(3^{(0.75-0.5\omega)n})$
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 $S_m^{n/2} \coloneqq \{-m, \dots, m\}^{\frac{n}{2}}, \quad \mathsf{H} \colon \{0, 1\}^* \to S_m^{n/2}, \quad f_0 \colon \mathbf{v} \mapsto \mathsf{H}(\mathbf{v} \star x), \text{ and } \quad f_1 \colon \mathbf{v} \mapsto \mathsf{H}\big((-\mathbf{v}) \star y\big)$

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- Partial Representation (This work): This time $f_i: D_i \to D$ where $D := \mathcal{T}^{\frac{(1-\delta)n}{2}}(1/3) \times \mathcal{T}^{\delta n}(\alpha)$,

$$D_{0} := \mathcal{T}^{\frac{(1-\delta)n}{2}}(1/3) \times \{0\}^{\frac{(1-\delta)n}{2}} \times \mathcal{T}^{\delta n}(\alpha) \text{ and}$$

$$D_{1} := \{0\}^{\frac{(1-\delta)n}{2}} \times \mathcal{T}^{\frac{(1-\delta)n}{2}}(1/3) \times \mathcal{T}^{\delta n}(\alpha),$$
(1)

The solution is $\mathbf{v} = \mathbf{v}_0 + \mathbf{v}_1$.





(a) Complexity of PCS, MitM and the representation-based trade-off

(b) Complexity of PCS, the representation trade-off, and partial representations.





(a) Complexity of different approaches.



(b) Complexity for different choices of m.

Figure: On the left: Comparison of different representation based methods. On the right: Comparison of representation based methods for different *m*.



1 REGA overview



Adapting Techniques to the REGA-DLOG_m Setting





In the SQALEd-CSIDH [3], three concrete parameter instantiations for ternary-key are given, respectively, aiming at satisfying NIST security level L_1, L_2 and L_3 . To match the security definition of category L_i the authors impose restrictions on the memory and time complexity of $M_i = 2^{w_i}$ and $T_i = 2^{t_i}$ with

$$(w_1, w_2, w_3) = (80, 100, 119)$$
 and $(t_1, t_2, t_3) = (128, 128, 192).$

Additionally,

- The number of generators n_i are equal to $n_1 = 139$ for L_1 , $n_2 = 148$ for L_2 and $n_3 = 210$ for L_3 .
- The security of those parameter sets is determined via the PCS time-memory trade-off.
- In the memory restrictions, the authors of [3] conservatively ignore polynomial factors.

Consequently, it holds $M_i = 3^{c_i n_i} = 2^{w_i}$, which allows to determine the asymptotic memory exponent as $c_i = \frac{w_i}{n_i \cdot \log_2 3}$. Then, we obtain

- 1. $c_1 \approx 0.3631$ and running time $T_{\rm PCS} = 3^{0.5685n}$.
- 2. $c_2 pprox 0.4263$ and running time $T_{
 m PCS} = 3^{0.5369n}$.
- 3. $c_3 \approx 0.3575$ and running time $T_{
 m PCS} = 3^{0.5713n}$.



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- 1. $c_1 \approx 0.3631$ and running time $T_{PCS} = 3^{0.5685n}$. This work: $T_{Rep} = 3^{0.5316n}$ (gain of 8.13 bits).
- 2. $c_2 \approx 0.4263$ and running time $T_{\rm PCS} = 3^{0.5369n}$. This work: $T_{\rm Rep} = 3^{0.5174n}$ (gain of 4.57 bits).
- 3. $c_3 \approx 0.3575$ and running time $T_{\rm PCS} = 3^{0.5713n}$. This work: $T_{\rm Rep} = 3^{0.5330n}$ (gain of 12.75 bits).



Thanks for attending!



Source Strain Strai



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